2019 Laureates

Andrew B. Kahng | Professor, UC San Diego, USA

Education & Work Experience

1963  Born in San Diego, CA, USA
1984  A.B., Applied Mathematics, Harvard College, USA
1986  M.S., Computer Science, UC San Diego, USA
1989  Ph.D., Computer Science, UC San Diego, USA

1989 − 2000  Professor, UCLA, USA
1995 − 1997  Visting Scientist, Cadence Design Systems, USA
2001 −  Professor, UC San Diego, USA

Achievements

Dr. Andrew B. Kahng developed algorithms for IC (Integrated Circuit) physical design automation to maximize chip performance through optimal layout and connections of complex components on semiconductor chips. He shifted the paradigm of IC physical design by first proposing DFM (Design for Manufacturability) from the initial stage of IC design.

He advanced the development of the semiconductor industry as his innovative IC physical design technologies have become the standard for major semiconductor research institutes and manufacturers around the world.