

Modern Physical Design: Algorithm Technology Methodology (Conclusion)

Stan Chow Ammocore

Andrew B. Kahng UCSD

Majid Sarrafzadeh UCLA

So ...

- This tutorial covered "the latest word" in physical chip implementation methodology and physical design (PD) algorithm technology.
 - PART I: Technology and Methodology Context Setting
 - PART II: Fundamental Physical Design Formulation and Algorithms
 - PART III: Interaction with Upstream Floorplanning and Logic Synthesis
 - PART IV: Interaction with extraction, analysis, and performance validation
 - PART V: Linkage to custom Layout
- Feel free to contact us if you have any questions:
 - <http://vlsicad.ucsd.edu/ICCAD2000TUTORIAL>
 - abk@ucsd.edu
 - stan@ammocore.com
 - majid@cs.ucla.edu