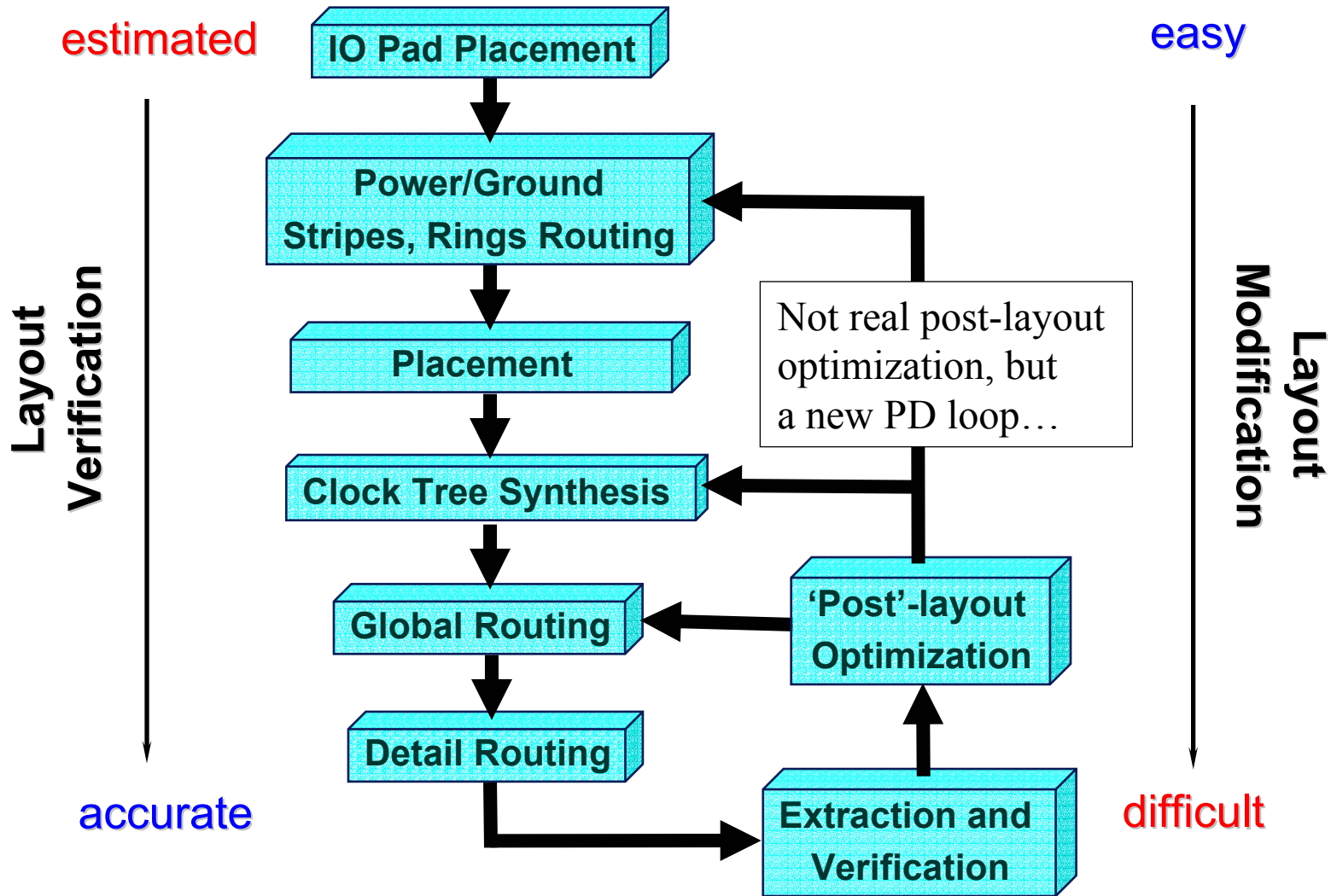
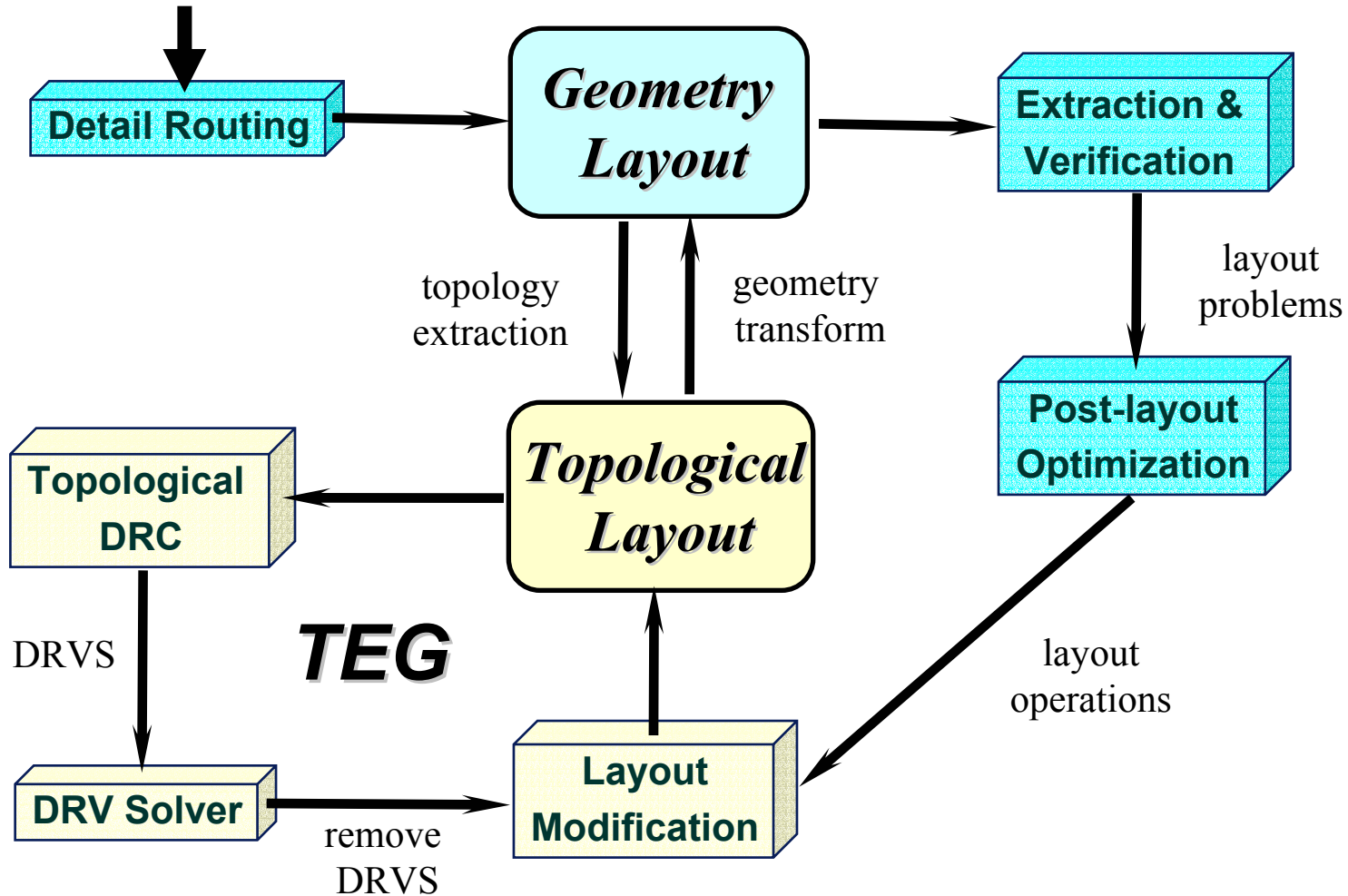


PD: Verification Vs. Modification



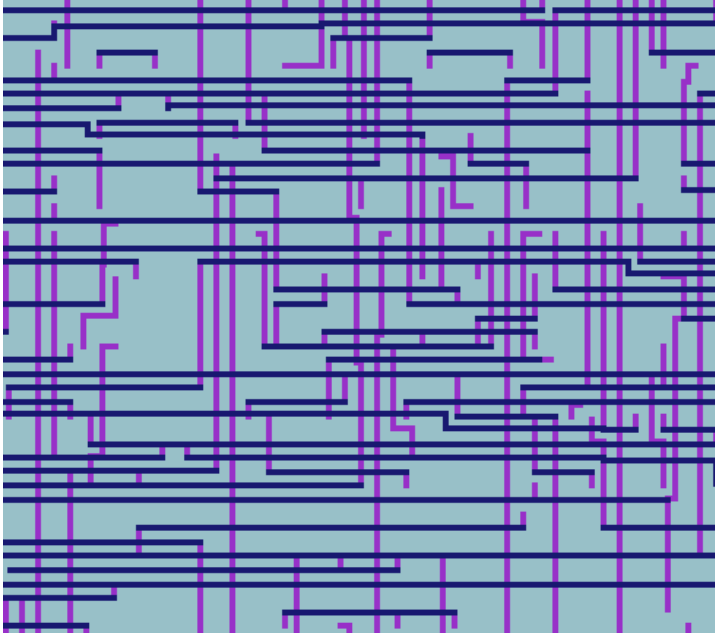
TEG: A Post-layout Optimization Method



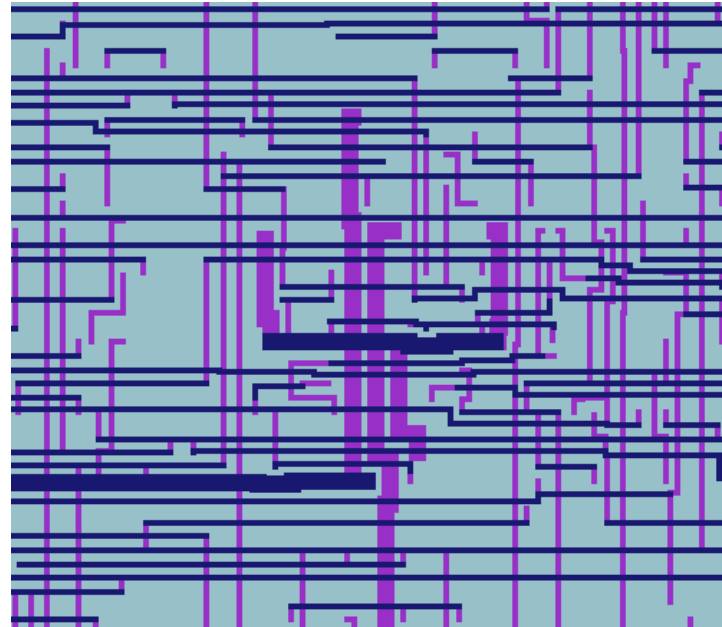
To be appeared in ISPD'02



Wire Sizing by TEG



Original Layout
(LEF/DEF)

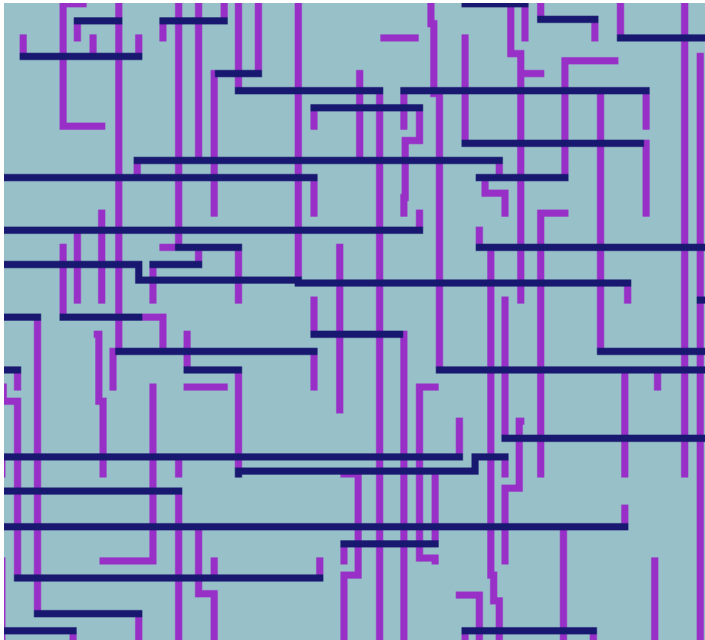


After wire sizing
(LEF/DEF)

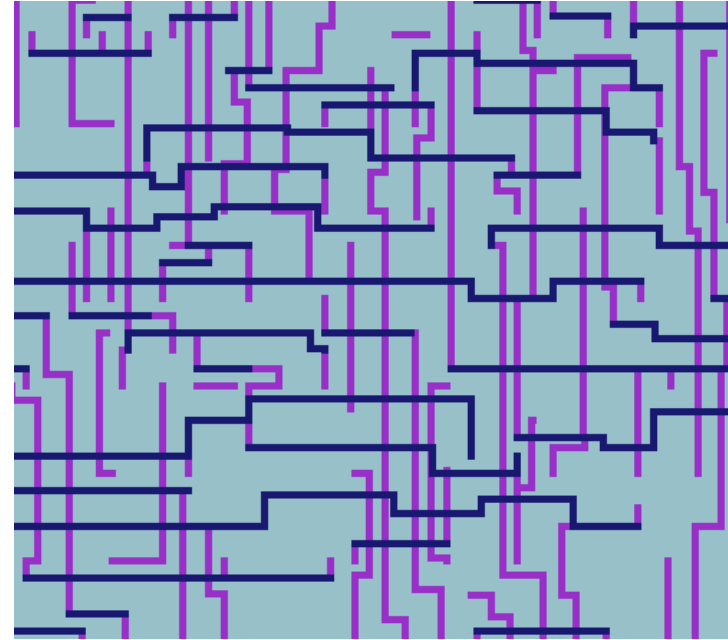
Fix crosstalk-delay, reduce IR-drop ...



Wire Distribution by TEG



Original Layout



After wire distribution

Decrease CMP process variation, improve yield and manufacturability...

